

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,737,749 B2  
DATED : May 18, 2004  
INVENTOR(S) : Nayon Tomsio and Avi Liebermensch

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4,

Line 8, the words "output match resistor output" should read -- output match resistor  $R_{m_{output}}$  --

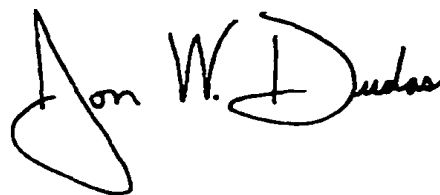
Column 7,

Line 15, the words "The circuit package as recited in claim 14" should read -- The circuit as recited in claim 14 --.

Line 41, the words "and second said conductive layer" should read -- and said conductive layer --.

Signed and Sealed this

Sixteenth Day of November, 2004

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large loop for the 'J' and a cursive 'D'.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*